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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant(s):	Richard S. Wise (et al.)	Examiner:	Julio Maldonado
Serial No.:	10/709,204	Group Art Unit:	2823
Filed:	April 21, 2004	Attorney Docket No:	FIS920030028
Title:	WIRING STRUCTURE FOR INTEGRATED CIRCUIT WITH REDUCED INTRALEVEL CAPACITANCE		

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**RESPONSE**

Mail Stop Non-Fee Amendment  
Assistant Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

This paper is submitted in response to an Office Action mailed January 10, 2006 by the United States Patent and Trademark Office in connection with the above-identified Application. A response to this January 10, 2006 Office Action is due by April 10, 2006. Therefore, this paper is being timely filed.

Remarks begin on page 2 of this paper.

FIS920030028